

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| S98 | 579 | 716/3.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/15 16:00 |
| S97 | 1 | ((map\$4 adj2 function) same configuration same (HDL hardware adj description adj language) same (database file) same (configur\$5 adj latch) same (input output) same value same transformation).clm. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/15 16:00 |
| S96 | 1365 | 716/1.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/15 13:45 |
| S95 | 1323 | 716/6.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/14 13:30 |
| S94 | 1595 | 716/5.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/14 13:30 |
| S93 | 2144 | 716/4.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/14 10:43 |
| S92 | 15 | S85 and S87 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:53 |
| S91 | 2 | S85 and S86 and S87 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:53 |

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| S90 | 47 | S85 and S86 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:53 |
| S89 | 2 | S85 and S86 and S87 and S88 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:53 |
| S88 | 148 | (read\$3 writ\$3) with (value input) same mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:52 |
| S87 | 6363 | mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:50 |
| S86 | 263 | instantiat\$3 same configur\$5 same (FF latch register flip adj2 flop) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:49 |
| S85 | 756 | (hdl or vhdl or verilog or hardware adj description adj language) same (file statement) and configur\$5 same (construct latch register FF flip adj2 flop) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:47 |
| S84 | 104 | (hdl or vhdl or verilog or hardware adj description adj language) and configur\$5 same (latch register FF flip adj2 flop) and mapping adj2 function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:45 |
| S82 | 62 | (hdl or vhdl or verilog or hardware adj description adj language) and (configur\$5 adj2 (latch register FF flip adj2 flop)) and mapping adj2 function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:43 |

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| S83 | 2 | (hdl or vhdl or verilog or hardware adj description adj language) and (configur\$5 adj2 (latch register FF flip adj2 flop)) and mapping adj2 function and dial | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:42 |
| S81 | 34 | (hdl or vhdl or verilog or hardware adj description adj language) and (configur\$5 adj2 (latch register FF flip adj2 flop)) and mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:41 |
| S80 | 2 | (hdl or vhdl or verilog or hardware adj description adj language) same statement and (configur\$5 adj2 (latch register FF flip adj2 flop)) and mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:32 |
| S79 | 1 | (hdl or vhdl or verilog or hardware adj description adj language) with statement and (configur\$5 adj2 (latch register FF flip adj2 flop)) and mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:32 |
| S78 | 1 | (hdl or vhdl or verilog or hardware adj description adj language) with statement and (configur\$5 adj2 (latch register FF flip adj2 flop)) and (language simulat\$3 software) and (database file) and mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:32 |
| S77 | 1 | (hdl or vhdl or verilog or hardware adj description adj language) with statement same (configur\$5 adj2 (latch register FF flip adj2 flop)) and (language simulat\$3 software) and (database file) and mapping adj function | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:31 |
| S76 | 26 | (hdl or vhdl or verilog or hardware adj description adj language) with statement same (configur\$5 adj2 (latch register FF flip adj2 flop)) and (language simulat\$3 software) and (database file) and simulation | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/03/08 10:30 |
| S2 | 2 | "20050149883" and dial and configurat\$3 adj latch | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/10 17:44 |